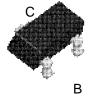


**FMMT449** 



Е

## SuperSOT<sup>™</sup>-3

# **NPN Low Saturation Transistor**

These devices are designed with high current gain and low saturation voltage with collector currents up to 2A continuous. Sourced from Process NB.

#### 

Symbol	Parameter	FMMT449	Units
V <sub>CEO</sub> Collector-Emitter Voltage		30	V
V <sub>CBO</sub> Collector-Base Voltage		50	V
V <sub>EBO</sub> Emitter-Base Voltage		5	V
IC Collector Current - Continuous - Peak Pulse Current		1 2	A
T <sub>J,</sub> T <sub>stg</sub>	Operating and Storage Junction Temperature Range	-55 to +150	°C

\*These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

#### NOTES:

1) These ratings are based on a maximum junction temperature of 150°C.

2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

## Thermal Characteristics TA = 25°C unless otherwise noted

Symbol	Characteristic	Max U		
		FMMT449		
PD	Total Device Dissipation* Derate above 25°C	500 4	mW mW/°C	
$R_{\theta JA}$ Thermal Resistance, Junction to Ambient250°C/W				
*Device mou	nted on FR-4 PCB 4.5" X 5"; mounting pad 0.02 in <sup>2</sup> of 2oz copper.			

© 1998Fairchild Semiconducto Corporation

## **NPN Low Saturation Transistor**

(continued)

### **Electrical Characteristics**

Electrical Characteristics T <sub>A = 25°C unless otherwise noted</sub>						
Symbol	Parameter		Test Conditions	Min	Max	Units

### OFF CHARACTERISTICS

BV <sub>CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = 10 mA	30		V
BV <sub>CBO</sub>	Collector-Base Breakdown Voltage	I <sub>C</sub> = 1mA	50		V
BV <sub>EBO</sub>	Emitter-Base Breakdown Voltage	I <sub>E</sub> = 100 μA	5		V
I <sub>CBO</sub>	Collector Cutoff Current	V <sub>CB</sub> = 40 V V <sub>CB</sub> = 40 V, Ta=100°C		100 10	nA uA
I <sub>EBO</sub>	Emitter Cutoff Current	$V_{EB} = 4V$		100	nA

### **ON CHARACTERISTICS\***

h <sub>FE</sub>	DC Current Gain	I <sub>C</sub> = 50 mA, V <sub>CE</sub> = 2V	70		-
		I <sub>C</sub> = 500 mA, V <sub>CE</sub> = 2V	100	300	
		$I_C = 1A, V_{CE} = 2V$	80		
		$I_C = 2A, V_{CE} = 2V$	40		
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = 1 A, I <sub>B</sub> = 100 mA		500	mV
		$I_{C} = 1 \text{ A}, I_{B} = 100 \text{ mA}$ $I_{C} = 2 \text{ A}, I_{B} = 200 \text{ mA}$		1.0	V
V <sub>BE(sat)</sub>	Base-Emitter Saturation Voltage	I <sub>C</sub> = 1 A, I <sub>B</sub> = 100 mA		1.25	V
V <sub>BE(on)</sub>	Base-Emitter On Voltage	I <sub>C</sub> = 1 A, V <sub>CE</sub> = 2 V		1	V

### SMALL SIGNAL CHARACTERISTICS

C <sub>obo</sub>	Output Capacitance	V <sub>CB</sub> = 10 V, I <sub>E</sub> = 0, f = 1MHz		15	pF
f <sub>T</sub>	Transition Frequency	$I_{C} = 50 \text{mA}, V_{CE} = 10 \text{ V}, \text{ f}=100 \text{MHz}$	150		MHz

\*Pulse Test: Pulse Width  $\leq$  300 µs, Duty Cycle  $\leq$  2.0%

#### TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

ACEx<sup>TM</sup> CoolFET<sup>TM</sup> CROSSVOLT<sup>TM</sup> E<sup>2</sup>CMOS<sup>TM</sup> FACT<sup>TM</sup> FACT Quiet Series<sup>TM</sup> FAST<sup>®</sup> FAST<sup>®</sup> FAST<sup>TM</sup> GTO<sup>TM</sup> HiSeC<sup>TM</sup> ISOPLANAR<sup>™</sup> MICROWIRE<sup>™</sup> POP<sup>™</sup> PowerTrench<sup>™</sup> QS<sup>™</sup> Quiet Series<sup>™</sup> SuperSOT<sup>™</sup>-3 SuperSOT<sup>™</sup>-6 SuperSOT<sup>™</sup>-8 TinyLogic<sup>™</sup> UHC<sup>™</sup> VCX<sup>™</sup>

## DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

#### LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION. As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user. 2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

#### **PRODUCT STATUS DEFINITIONS**

Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.